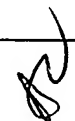


<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/626,656	TAKADA ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
Craig A. Thompson	2813		

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to communication of 4/8/2004.
2. ☒ The allowed claim(s) is/are 1-21.
3. ☒ The drawings filed on 25 July 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date <u>4/20/03</u> | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material                              | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____.   |

### REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: prior art of record does not describe or suggest applicants' invention set forth in claims 1-8 wherein a first semiconductor is formed, a first transparent electroconductor layer is formed on the first semiconductor layer, and a second semiconductor layer is formed such that the method comprises executing a passivation treatment on defects in the first semiconductor layer before the step of forming the second semiconductor layer. Similarly, prior art of record does not describe or suggest applicants' invention set forth in claims 9-20 wherein a second transparent layer is also formed on the second semiconductor layer and passivation treatments are carried out on the defects in the first semiconductor layer before the formation of the second semiconductor layer as well as on the second semiconductor layer. Finally, prior art of record does not describe or suggest the device of claim 21 wherein a semiconductor device comprises a substrate, a first semiconductor layer formed on the substrate and having defects on it, a first transparent electroconductive layer formed on areas other than the defective areas, and a second semiconductor layer formed on the first transparent electroconductive layer.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Cited Prior Art***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Midorikawa et al. (US Patent No. 6,132,585) teaches a semiconductor element with a conductor, semiconductor layer and transparent conductor layer being sequentially formed in conjunction with a passivation treatment (abstract) but teaches selective coverage with an insulator (see claim 1).

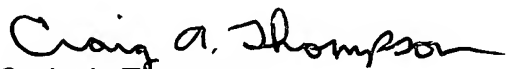
Art Unit: 2813

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Craig A. Thompson whose telephone number is (571)272-1699. The examiner can normally be reached on Monday-Friday 8:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on (571)272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
Craig A. Thompson  
Primary Examiner  
Art Unit 2813

10 December 2004